**LESSON PLAN**

Branch/ Semister/Session : II ECE (C) (1st semester)

Academic Year : 2016 – 2017

Subject Name : Electronic Circuits – 1(EC1)

Faculty Name : P V MURALIDHAR

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| Period | Date(tentative) | Topic | Unit no | Teaching methodology |
| 1 | 27-06-16 | Introduction to Harmonic components | I | Class room teaching |
| 2 | 28-06-16 | Capacitive filter |
| 3 | 29-06-16 | Inductive filter |
| 4 | 30-06-16 | LC filter |
| 5 | 04-07-16 | Π filter |
| 6 | 05-07-16 | Problems |
| 7 | 06-07-16 | Problems |
| 8 | 11-07-16 | Zener diode as regulator |
| 9 | 12-07-16 | Zener diode as regulator |
| 10 | 13-07-16 | Problems |
| 11 | 14-07-16 | Operating point, Need of biasing | II | Class room teaching |
| 12 | 18-07-16 | Stability factors & fixed bias methods (BJT) |
| 13 | 19-07-16 | Self bias & collector to base bias (BJT) |
| 14 | 20-07-16 | Voltage divider bias (BJT), Thermal runaway |
| 15 | 21-07-16 | Compensation techniques |
| 16 | 01-08-16 | Thermal resistance & thermal stability |
| 17 | 02-08-16 | FET biasing |
| 18 | 03-08-16 | MOSFET biasing |
| 19 | 04-08-16 | Problems |
| 20 | 08-08-16 | Problems |
| 21 | 09-08-16 | Problems | III | Class room teaching |
| 22 | 16-08-16 | Problems |
| 23 | 17-08-16 | Problems |
| 24 | 18-08-16 | Problems |
| 25 | 22-08-16 | Two port network representation |
| 26 | 23-08-16 | h-parameter model |
| 27 | 24-08-16 | CB configuration equivalent |
| 28 | 29-08-16 | CE & CC configuration equivalent |
| 29 | 30-08-16 | Equivalence between CB, CE and CC configuration |
| 30 | 31-08-16 | Measurement of h-parameters |
| 31 | 01-09-16 | Analysis transistor amplifier using h-parameter model |
| 32 | 06-09-16 |
| 33 | 07-09-16 | Problems |
| 34 | 08-09-16 | FET low frequency model |
| 35 | 13-09-16 | h-parameter analysis of CE amplifier | IV | Class room teaching |
| 36 | 14-09-16 | h-parameter analysis of CE amplifier | IV | Class room teaching |
| 37 | 15-09-16 | h-parameter analysis of emitter follower |
| 38 | 19-09-16 | h-parameter analysis of CB amplifer |
| 39 | 20-09-16 | Miller theorem and dual |
| 40 | 21-09-16 | FET CS amplifier analysis |
| 41 | 22-09-16 | FET CD amplifier analysis |
| 42 | 26-09-16 | FET CG amplifier analysis |
| 43 | 27-09-16 | Problems |
| 44 | 28-09-16 | Problems |
| 45 | 29-09-16 | Problems |
| 46 | 03-10-16 | Problems |
| 47 | 04-10-16 | Transistor HF model & parameter explanation | V | Class room teaching |
| 48 | 05-10-16 | Hybrid conductance parameters |
| 49 | 06-10-16 | Hybrid capacitance parameters |
| 50 | 13-10-16 | Variations in hybrid parameters |
| 51 | 17-10-16 | CE short circuit current gain |
| 52 | 18-10-16 | CE current gain with resistive load |
| 53 | 19-10-16 | CE amplifier response |
| 54 | 20-10-16 | Gain bandwidth product |
| 55 | 24-10-16 | Emitter follower at high frequency |
| 56 | 25-10-16 | FET at high frequency |
| 57 | 26-10-16 | Problems |
| 58 | 27-10-16 | Problems |